

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE ("USPTO")

First Named Inventor: Makoto NAKANISHI

Confirmation No.: 6442

Serial No.: 10/798,287

Examiner: Ngo, Chuong D

Filed: March 12, 2004

Group Art Unit: 2193

Title: MATRIX PROCESSING DEVICE IN SMP NODE DISTRIBUTED MEMORY TYPE
PARALLEL COMPUTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Box AF

AMENDMENT AFTER FINAL REJECTION

Sir:

In response to the Office Action dated July 21, 2009, whose period for response is October 21, 2009, kindly amend the above-identified application as set forth below. In view of the same as well as the following remarks, kindly reconsider and further examine the application.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.